



XA-9640
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Toshihiro TANAKA et al.

Appln. No.: 10/083,399

Group Art Unit: 2133

Filed: February 27, 2002

Examiner: P. Chung

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND A METHOD OF
TESTING THE SAME

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AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In response to the non-final Office Action mailed on
June 14, 2005, please amend the above-identified patent
application as indicated below.

Amendments to the claims are reflected in the listing
of claims, which begins on page 2 of this paper.

Amendments to the drawings begin on page 14 of this
paper and include 3 attached replacement sheets.

Remarks/Arguments begin on page 15 of this paper.